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(71) Applicant(s)

LG. Philips LCD Co Ltd  
(Incorporated in the Republic of Korea)  
20 Yoïdo-dong, Youngdungpo-ku, Seoul,  
Republic of Korea

(72) Inventor(s)

Won-Kyu Park  
Yun-Ho Jung  
Se-Jin Chung

(74) Agent and/or Address for Service

Edward Evans & Co  
Chancery House, 53-64 Chancery Lane, LONDON,  
WC2A 1SD, United Kingdom

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(52) UK CL (Edition Q )

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(56) Documents Cited

WO 97/45827 A1 US 5496768 A  
App.Phys.Lett. Vol 70, No 12, 23/6/97, pp 3434-3436, SI  
James et al, "Single crystal Si films" App.Phys.Lett.,  
Vol 69, No 19, 4/11/96, pp 2864-2866, RS Sposili & SI  
James, "Thin silicon films"

(58) Field of Search

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INT CL<sup>6</sup> H01L 21/20  
Online: WPI, JAPIO, EPODOC, INSPEC

(54) Abstract Title

Method for crystallizing amorphous silicon layer and method for fabricating tft

(57) An active layer commonly used in a thin-film-transistor is made by irradiating an amorphous silicon layer with a laser source at an energy density sufficient to induce substantially complete melting to form a melted region and an unmelted region. The melted region of the amorphous silicon layer is solidified with a lateral grain growth from the unmelted region to the melted region. Then the amorphous silicon layer is translated relative to the laser source. In such an apparatus, the laser source is prepared by emitting a laser beam through a mask 160. The mask 160 has a plurality of transparent regions 162 which comprises slits arranged adjacent to or next to each other and separated by a predetermined distance in certain applications. Such pattern may include contiguous chevron-shaped lines with curved apexes. Alternatively, the pattern includes slim rectangular apertures or dots. The size of the crystal grains may be increased by rotating the pattern through 90° and repeating the procedure.



Fig. 7A

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FIG. 1A (RELATED ART)

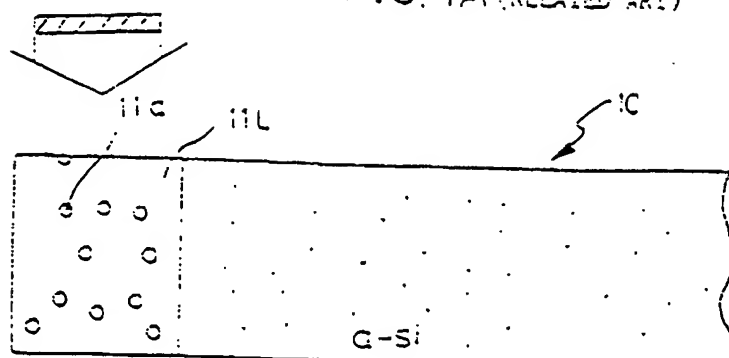


FIG. 1B (RELATED ART)

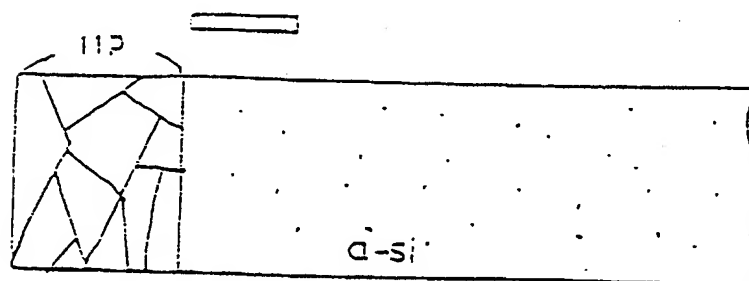


FIG. 1C (RELATED ART)

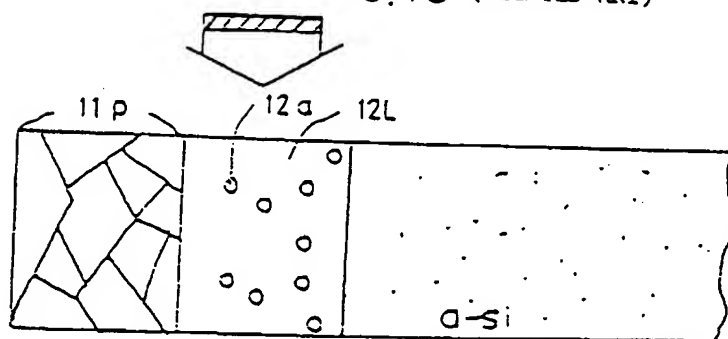


FIG. 1D (RELATED ART)

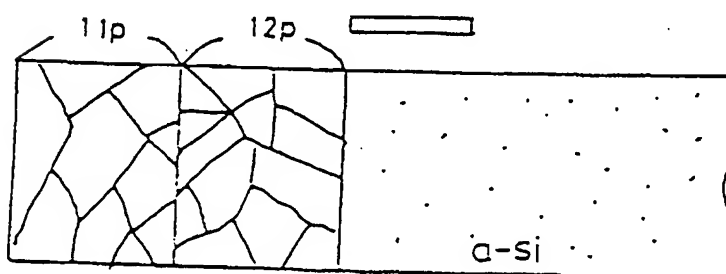
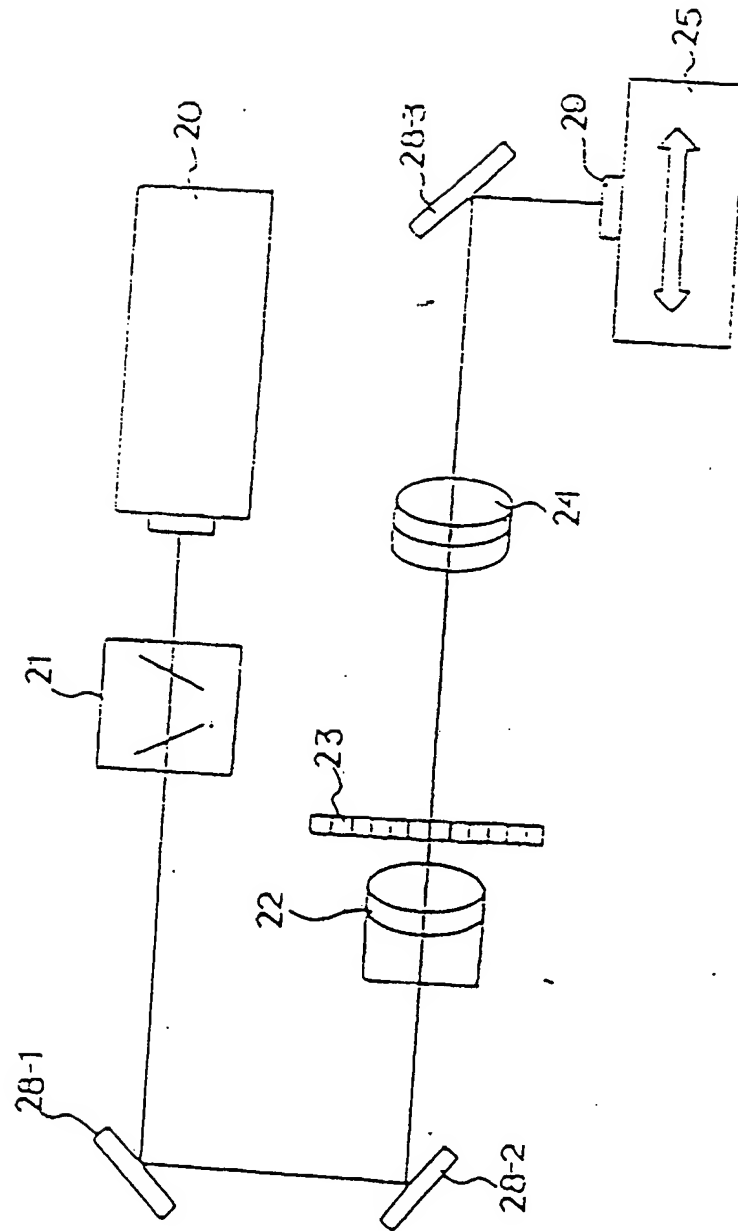
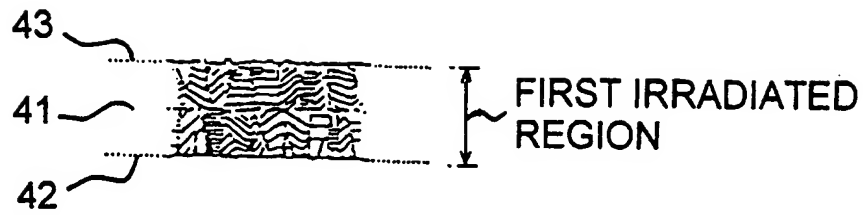
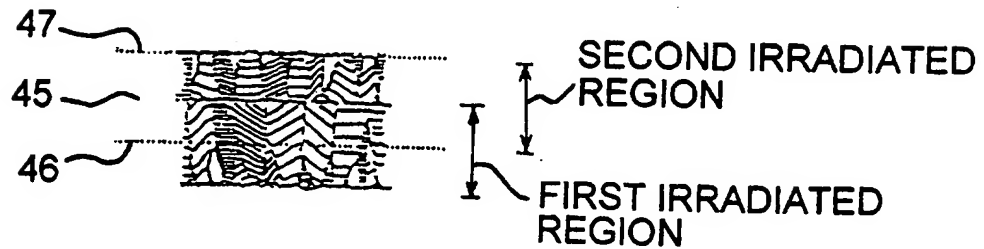


FIG. 2 (RELATED ART)

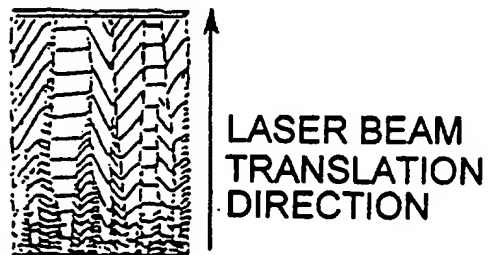




**Fig. 3A**  
(RELATED ART)

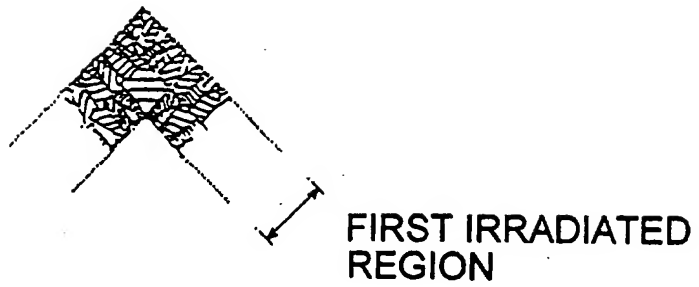


**Fig. 3B**  
(RELATED ART)



**Fig. 3C**  
(RELATED ART)

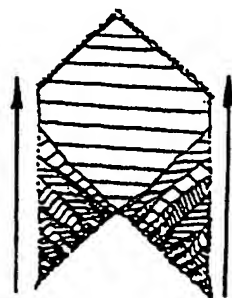
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**Fig. 4A**  
(RELATED ART)



**Fig. 4B**  
(RELATED ART)



**Fig. 4C**  
(RELATED ART)

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Fig. 5

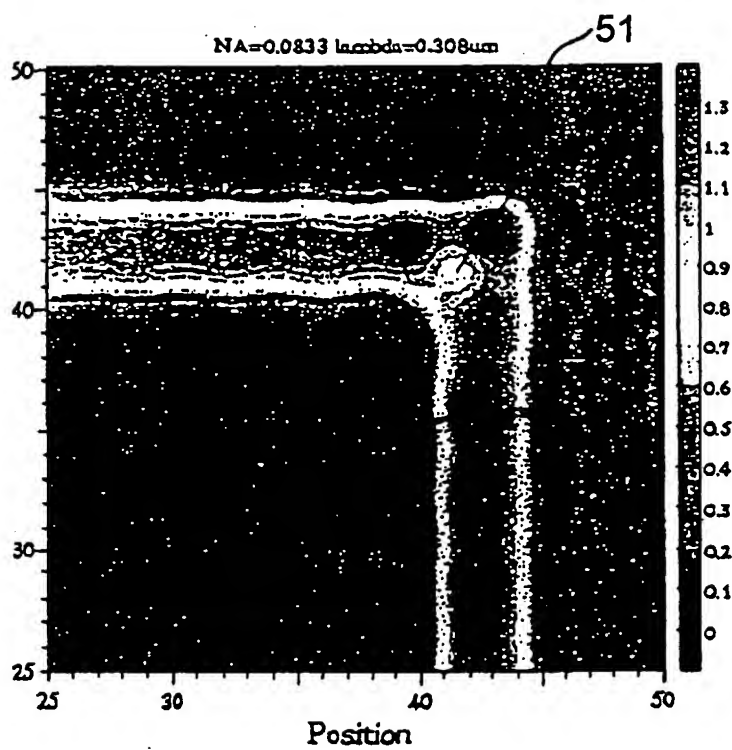


Fig. 6

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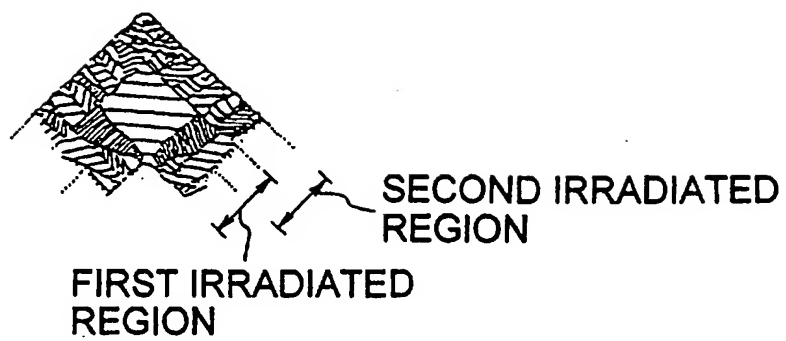


Fig. 7A

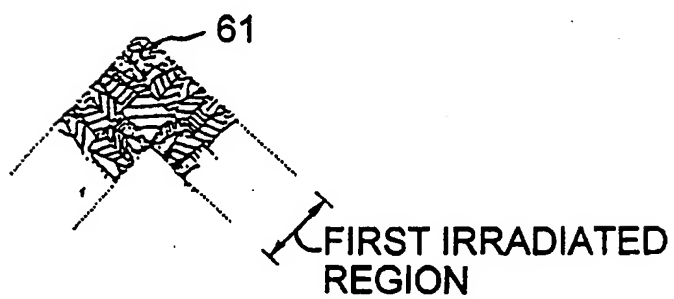


Fig. 7B

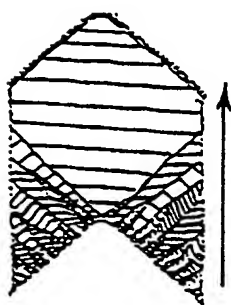


Fig. 7C

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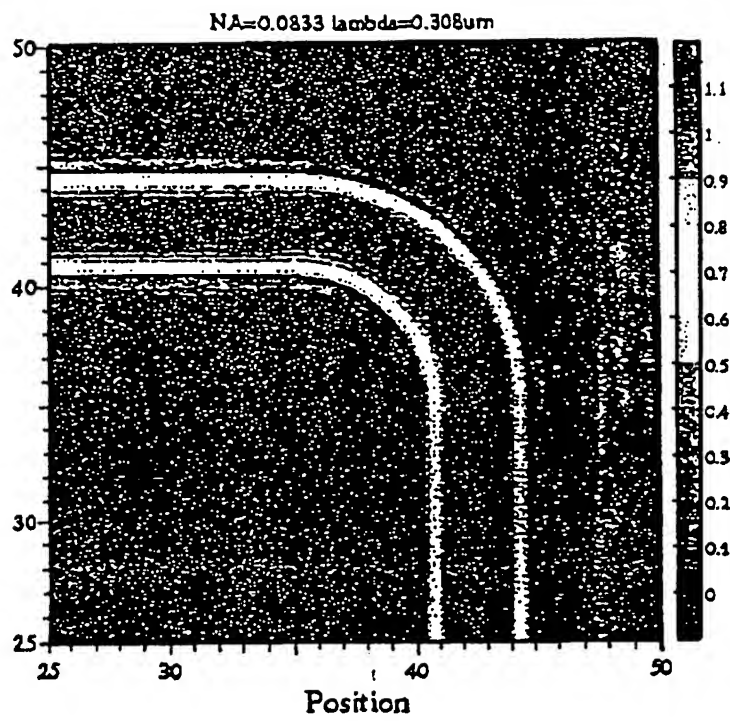


Fig. 8



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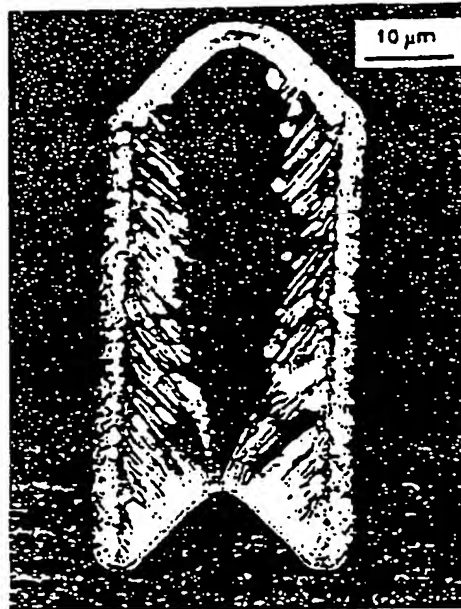


Fig. 9

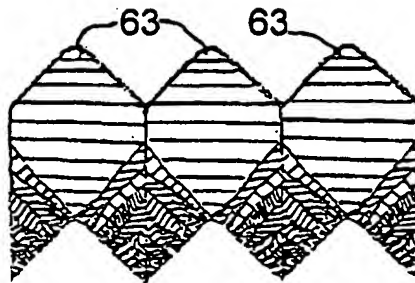


Fig. 10

FIG. 11

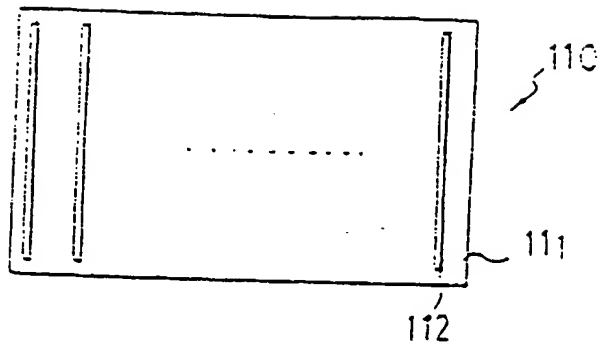


FIG. 12A

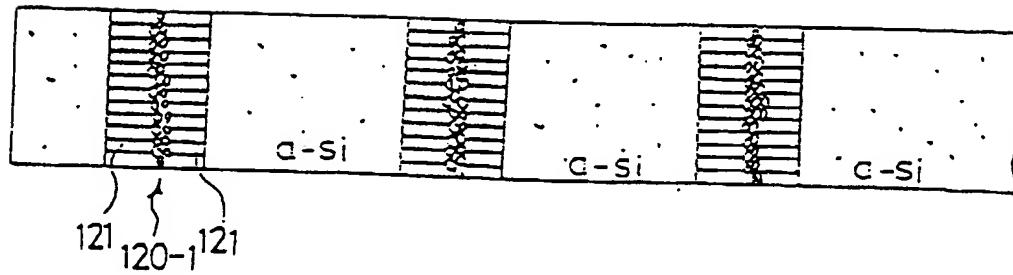


FIG. 12B

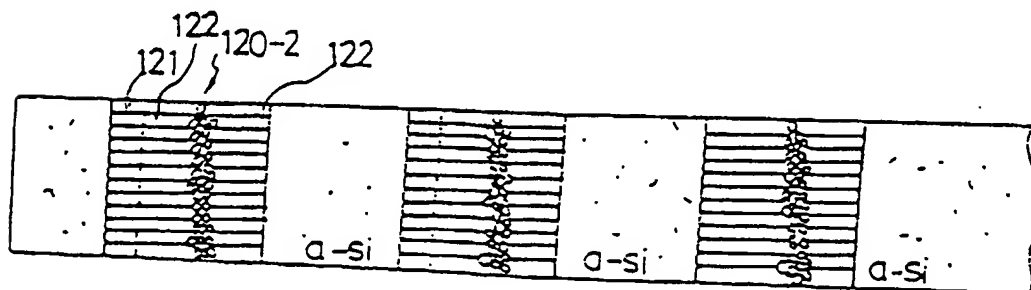


FIG.12C

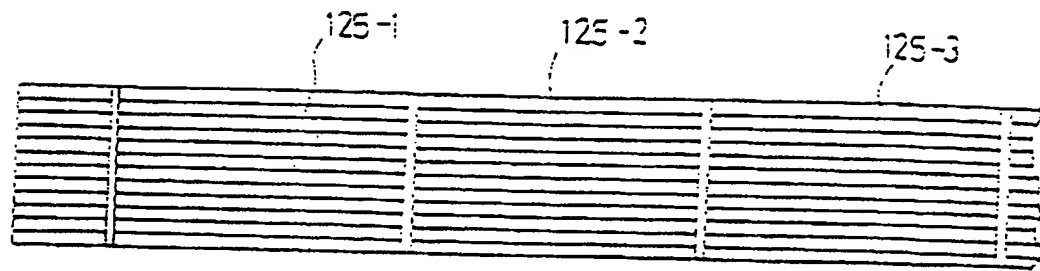


FIG.13A

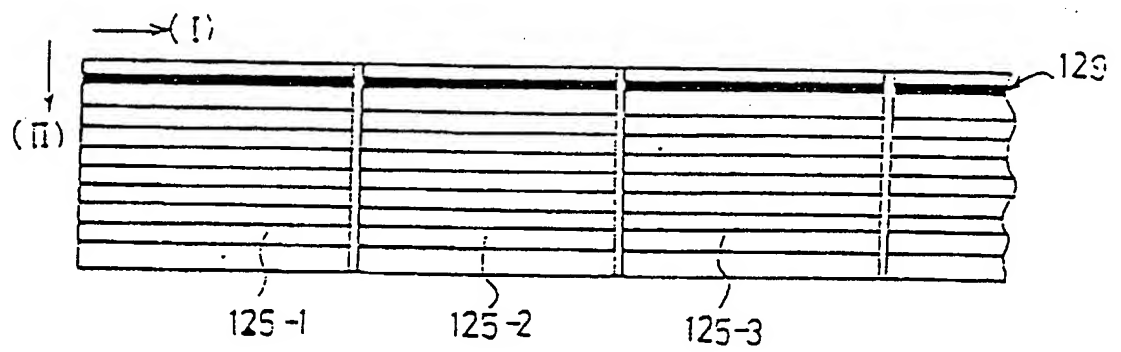


FIG.13B

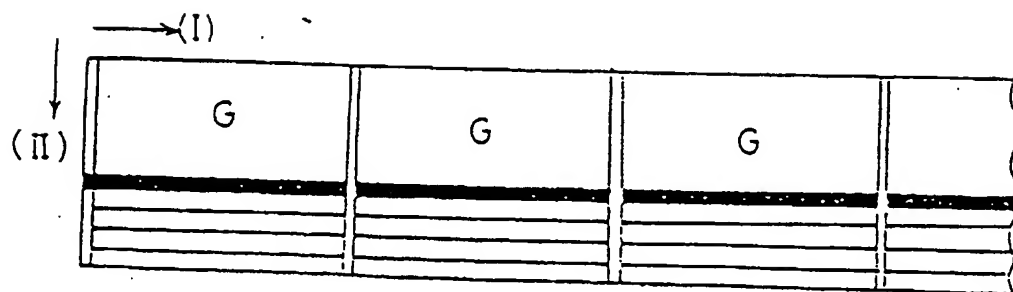


FIG.13C

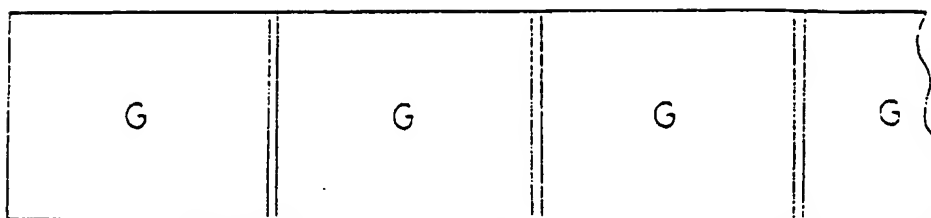


FIG.14

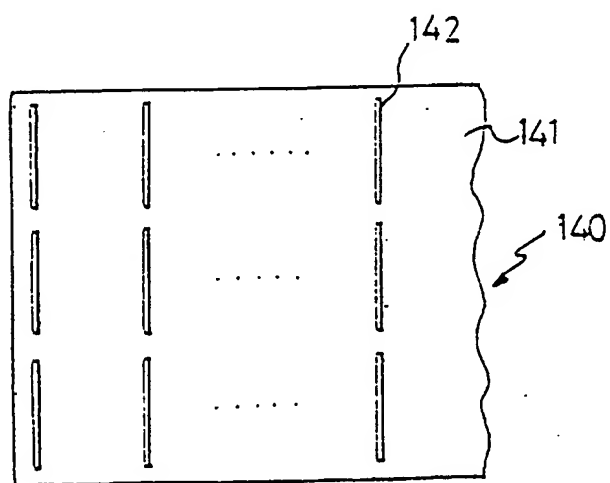


FIG.15A

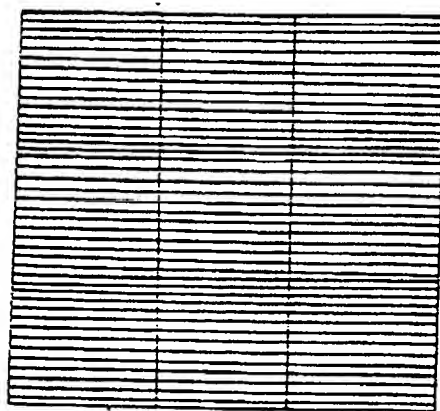


FIG.15B

G	G	G
G	G	G
G	G	G

FIG. 16

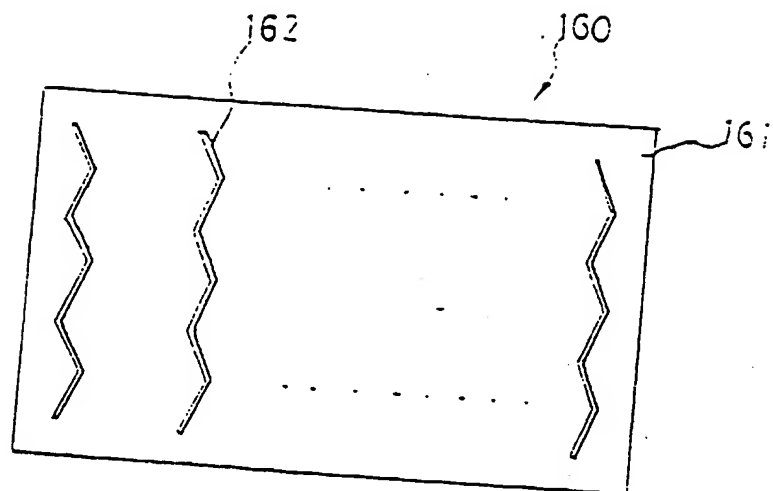


FIG. 17

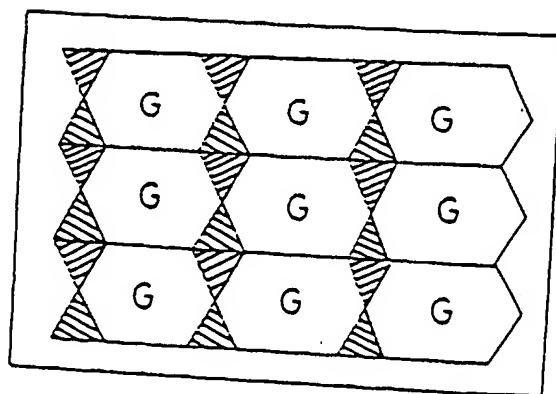


FIG. 18A

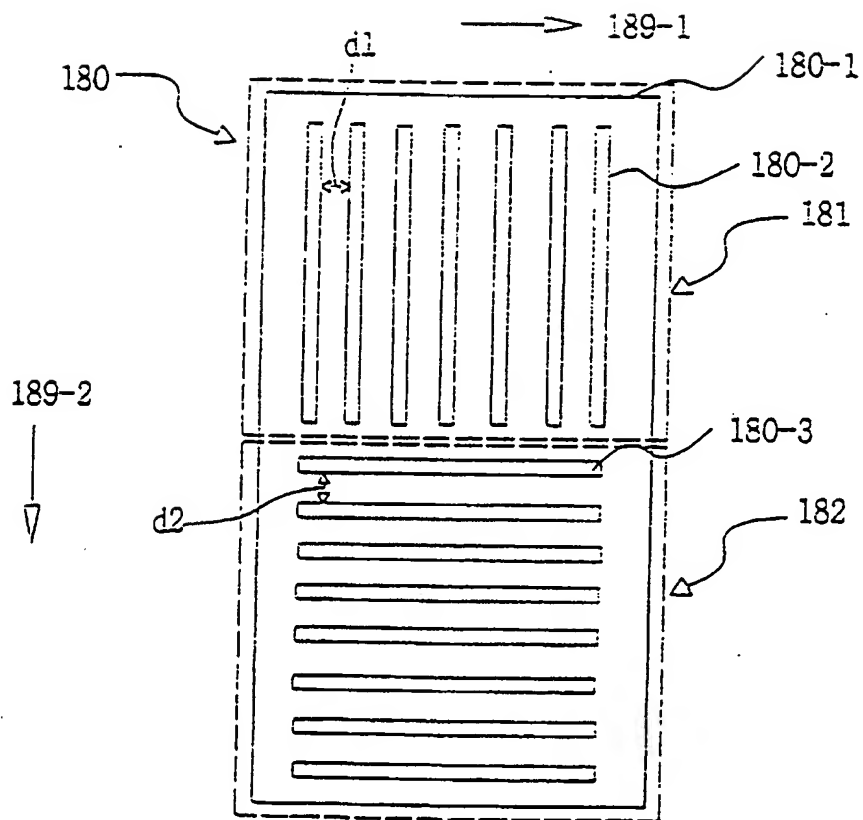


FIG. 18B

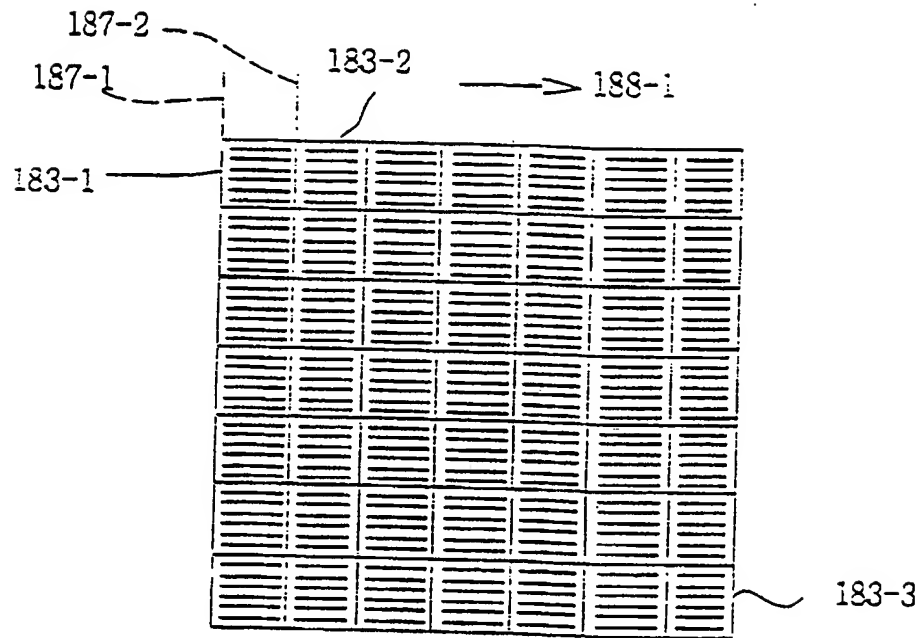
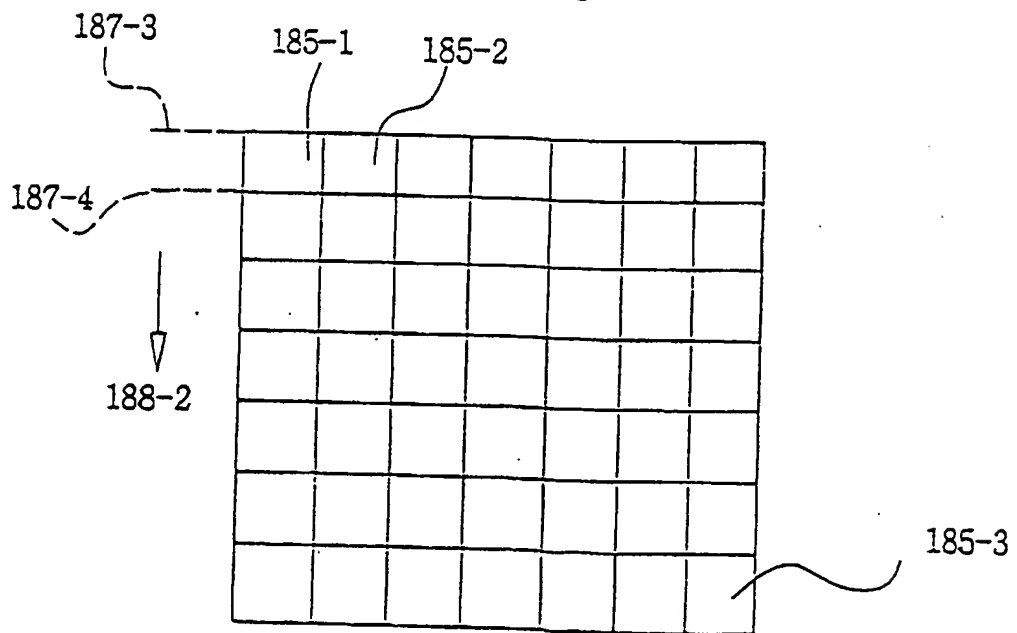


FIG. 18C





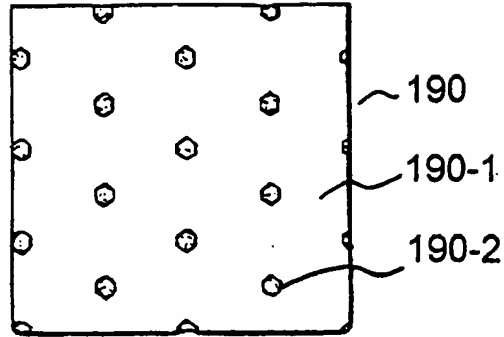
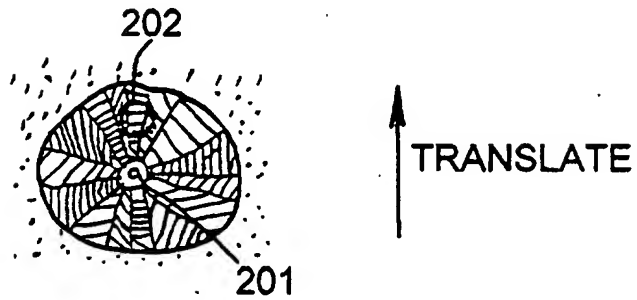
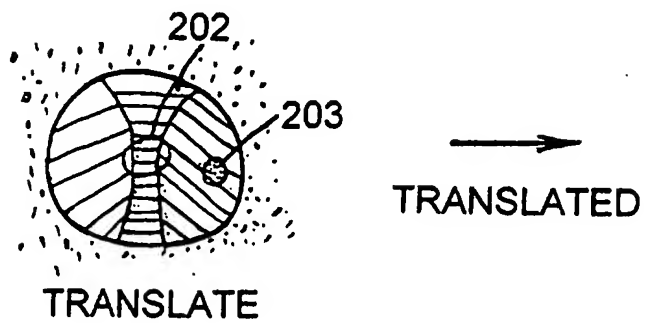


Fig. 19



TRANSLATED  
Fig. 20A



TRANSLATE  
Fig. 20B

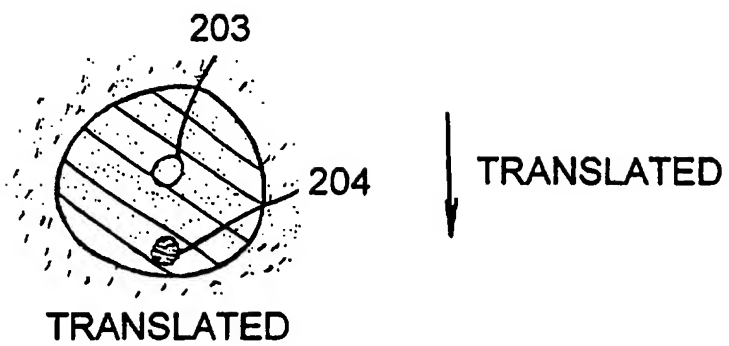


Fig. 20C

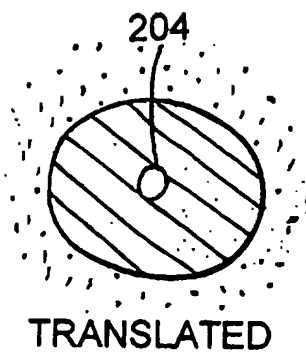


Fig. 20D

FIG. 21

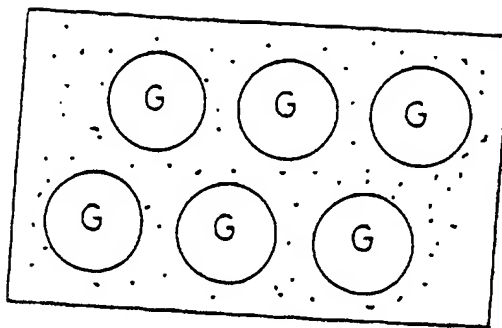


FIG. 22

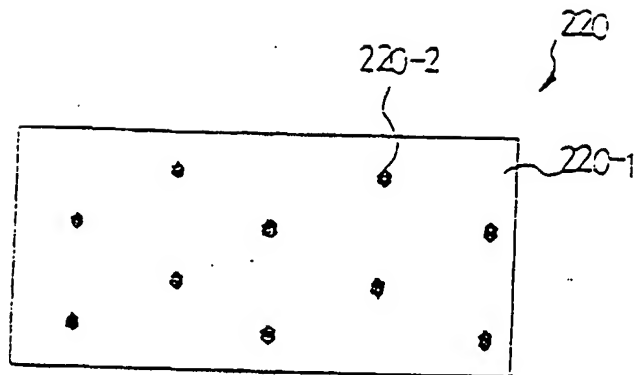


FIG. 23

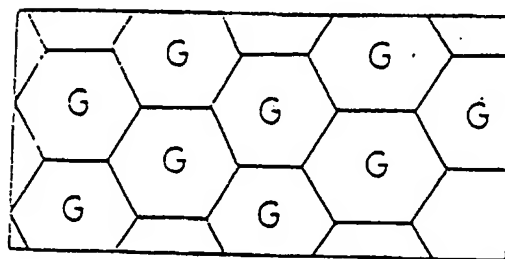


FIG. 24

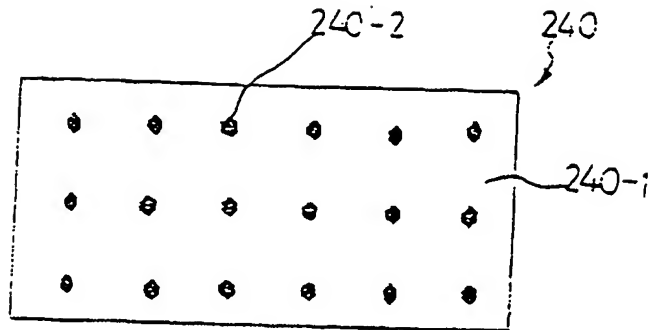


FIG. 25

G	G	G	G	G	G
G	G	G	G	G	G
G	G	G	G	G	G

FIG. 26A

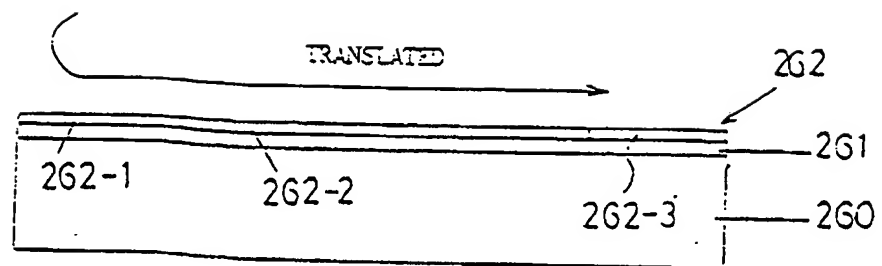


FIG. 26B

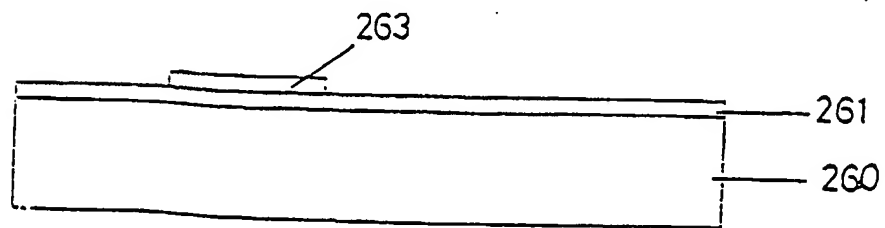


FIG. 26C

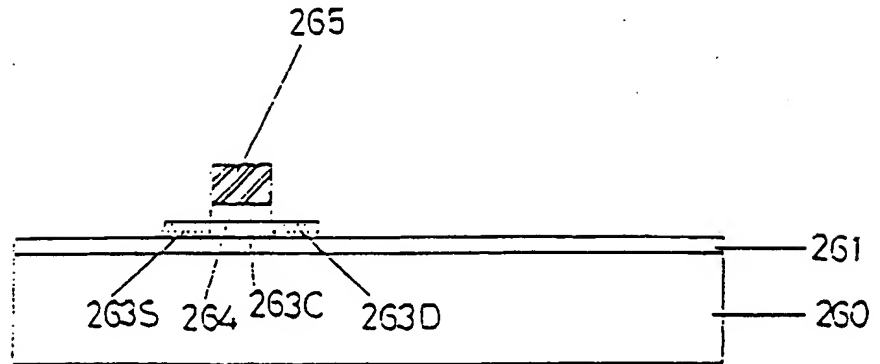


FIG. 26D

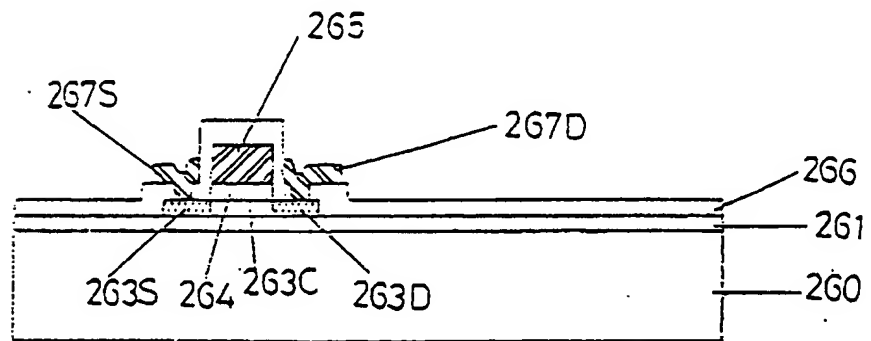
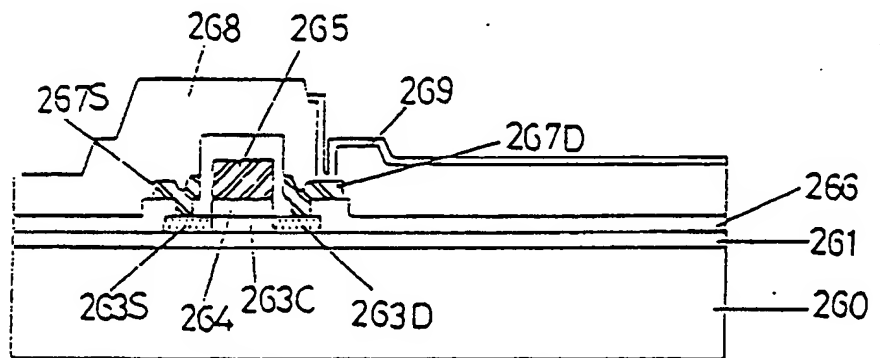


FIG. 26E



METHOD FOR CRYSTALLIZING AMORPHOUS SILICON LAYER AND  
METHOD FOR FABRICATING TFT.

5       The present invention relates to a method for crystallizing an amorphous silicon layer and a method for fabricating a thin film transistor (TFT) using the same, and more particularly, a method for crystallizing an amorphous silicon layer and fabricating a TFT by utilizing the Sequential Lateral Solidification (SLS) technique.

10       In order to fabricate TFTs on a low heat-resistant substrate, such as a glass substrate, an amorphous silicon layer or a polycrystalline silicon layer is deposited on the substrate and etched by photolithography to form active layers for TFTs.

      The mobility of charge carriers is low in the amorphous silicon layer. Accordingly, amorphous silicon TFT is not typically used as a driving circuit or a  
15   controller of a liquid crystal display (LCD). However, the mobility of charge carriers is high in the polycrystalline layer. Accordingly, polycrystalline TFTs can be used in driving circuits of a liquid crystal display (LCD), wherein devices for pixel array and driving circuits are formed simultaneously.

      There are two techniques for forming polycrystalline silicon film on a glass  
20   substrate. In the first technique, an amorphous silicon film is deposited on the substrate and crystallized at a temperature of about 600°C by Solid Phase Crystallization (SPC). This technique is difficult and problematic in terms of cost and materials because it requires a very high temperature.

      The second technique involves depositing an amorphous silicon film on the  
25   substrate and crystallizing the film by thermal treatment using a laser. The second technique is not a high temperature process, thus, this facilitates the formation of a polycrystalline silicon film on the glass substrate.

      Figs. 1A to 1D are schematic drawings which illustrate a method for forming a polycrystalline silicon film according to one related art. Referring to Fig. 1A, a  
30   particular region of an amorphous silicon film is first irradiated at an energy density to



induce formation of separated islands of amorphous silicon 11a and the silicon region 11L, a region generated by the irradiation of a laser beam, completely melts.

Referring to Fig. 1B, the amorphous film is translated relative to the laser beam over a distance less than the predetermined distance for a second irradiation. While the film is translating, the liquid silicon region 11L is crystallized under low temperature through a cooling process. The separated islands of amorphous silicon 11a are used as seeds for the crystallization process which results in the growth of the liquid silicon region 11L, thereby forming a first polycrystalline silicon region 11P. Grain growth occurs not only in the middle of the growth region, but growth also occurs in the interface between the liquid silicon region 11L and solid state amorphous silicon region a-Si. The grain growth stops when the grains collide at these grain boundaries.

Referring to Fig. 1C, a selected region of a translated amorphous silicon film is secondly irradiated. Thus, separated islands of amorphous silicon 12a remain and the other portions of the silicon, namely, silicon region 12L completely melts.

In Fig. 1D, the amorphous film is translated relative to the laser beam for the next irradiation. While the film is translating, the liquid silicon region 12L is crystallized under a low temperature cooling process. The separated islands of amorphous silicon 12a are used as seeds which grow into the liquid silicon region 12L, thereby forming a second polycrystalline silicon region 12P. Moreover, grain growth occurs at the interface between the liquid silicon region 12L and solid state amorphous silicon region a-Si, as well as at the interface between the liquid silicon region 12L and the first polycrystalline silicon region 11P.

The above described processes of irradiating and crystallizing are repeated over a total translation distance in order to crystallize the entire film. However, since the size of each silicon grain is not uniform and the location of the grain boundary varies in the polycrystalline silicon layer, a device-to-device uniformity is degraded in TFTs fabricated by such methods.

Accordingly, it is desirable and necessary to make the location of the grain boundary uniform and the grain size large.

Accordingly, it is proposed that a polycrystalline silicon film be formed on the glass substrate by using Sequential Lateral Solidification (SLS) techniques, as described in Robert S. Sposilli, M. A. Crowder, and James S. Im, Mat. Res. Soc. Symp. Proc. Vol. 452, 956~957, 1997. The SLS technique uses a phenomenon wherein the grain

5 boundaries in directionally solidified materials tend to form perpendicularly to the melted interface. The SLS technique enables the conversion of as-deposited amorphous or polycrystalline silicon films into a directionally solidified microstructure consisting of long, columnar grains.

The laser beam pattern for using SLS technique is prepared by an annealing  
10 apparatus as shown in Fig 2. An unpatterned laser beam is emitted from a light source 20 and is passed through an attenuator 21 to control the energy density of the unpatterned laser beam. The unpatterned laser beam is focused on a focus lens 22 and passed through a mask having a predetermined pattern 23 in order to pattern the laser beam. The patterned laser beam then passes through an imaging lens 24. A film 29 on a  
15 translation stage 25 is irradiated by the patterned laser beam. The entire film is scanned by the laser beam at a predetermined repetition rate. In this regard, Mirrors 28-1, 28-2, and 28-3 control the path of the laser beam.

Figs. 3A to 3C show a method for crystallizing an amorphous silicon film by the SLS technique according to another related art. Referring to Fig. 3A, a narrow region  
20 having a slit film shape, bounded by the dashed lines 42 and 43, is irradiated at an energy density sufficient to induce complete melting. Subsequently, lateral grain growth proceeds from the unmelted regions to the adjacent narrow strip region 41 which is fully-melted. Grain boundaries in directionally solidified materials proceed perpendicularly to the melt interface. Depending on the width of the molten region,  
25 lateral growth ceases when either of two events occur: (1) the two opposing growth fronts collide at the center, or (2) the molten region becomes sufficiently supercooled to cause bulk nucleation of solids.

Due to these restrictive events, the maximum lateral growth distance which can be achieved with a single pulse is limited depending on the film thickness and the incident  
30 energy density.

Referring to Fig. 3B, the film is translated relative to the beam image over a distance less than the single-pulse lateral growth distance and irradiated again. Lateral growth begins again from the edges of the completely molten region, located within the grains grown during the previous irradiation step. The length of the grains is increased beyond the single-pulse lateral growth distance. For example, a narrow region 45 bounded by dashed lines 46 and 47 is irradiated by a second laser pulse. Since one of these edges, in this case the edge 46, is located within the silicon region grown during the previous irradiation step, the lengths of the silicon grains formed by the previous irradiation are extended by the second irradiation beyond the single-phase lateral growth distance.

Referring to Fig. 3C, the above-described processes of irradiation and solidification can be repeated indefinitely, creating grains of any desired length. The final resultant microstructure is shown.

Using the method of the related art, a polycrystalline silicon film having uniform physical characteristics could be achieved through lateral growth of the silicon grain. However, the polycrystalline silicon film cannot be used to form devices for complicated circuits, whereas a single crystal silicon film may be use in the manufacture of such devices.

Figs. 4A to 4C illustrate a method for crystallizing an amorphous silicon film using the SLS technique according to a third related art. Referring to Fig. 4A, a selected region having a chevron-shaped aperture of the film and bounded by the dashed lines is irradiated at an energy density sufficient to induce complete melting. Subsequently, lateral grain growth proceeds from the unmelted region to the adjacent narrow strip, fully-melted region. The grain boundaries in directionally solidified materials tend to form perpendicularly in relation to the melt interface.

Because of the above-noted considerations, the maximum lateral growth distance that can be achieved with a single pulse is limited depending on the film thickness and the incident energy density. More specifically, the grain formed at the apex of the chevron experiences lateral growth not only in the translation direction, but also transverse to it, because the grain boundaries are formed perpendicularly to the melt

interface. Therefore, the negative curvature of the molten zone at the apex of the chevron leads to a widening of the grain, such that a single crystal silicon grain region is induced.

Referring to Fig. 4B, the film is translated relative to the beam image over a distance which is approximately one-half of the single-pulse lateral growth distance, and irradiated again. Lateral growth recommences from the edges of the completely molten region, located within the grains grown during the previous irradiation step. The length of the grains is increased beyond the single-pulse lateral growth distance. The grain formed at the apex of the chevron, a single crystal silicon region, widens.

As shown in Fig. 4C, the above-cited processes of irradiation and solidification can be repeated indefinitely, resulting in grains of any desired length. The final resultant microstructure is illustrated. As shown in Fig. 4C, the grain formed at the apex of the chevron, a single crystal silicon region, widens dramatically.

Fig. 5 shows TEM data, indicating a crystalline silicon film crystallized by using the chevron-shaped laser beam. This drawing shows a polycrystalline silicon region, having lateral columnar silicon grains grown perpendicular to the interface between the amorphous silicon region and the irradiated silicon region, and a single crystalline region formed from the apex of the chevron to its upper portion.

However, defects in the sub-boundaries areas can be found in the single crystalline region. Therefore, the crystallized silicon films formed by using a chevron-shaped laser beam do not yield TFT devices which can achieve the level of performance of silicon wafer-based TFT devices.

Fig. 6 shows the distribution of the energy density of an irradiated silicon film portion by a chevron-shaped laser beam. The energy density of the other portions, except the apex portion of the chevron-shaped beam, shows a normal distribution, the brim of which is low and the center is high. But the energy density of the apex portion of the chevron-shaped film is low relative the other portion which surrounds it. The explanation lies in the fact that the interference of the laser beam occurs at the apex because of the characteristics of the chevron-shaped beam. Thus, differences of the

thermal stress can form in the molten silicon portion irradiated by the apex of the laser beam. The principal defect lies in the interior of the crystallized silicon.

Accordingly, the present invention is directed to a method for crystallizing an  
5 amorphous silicon layer and a method for fabricating a TFT using the same, that substantially obviates one or more of the problems due to limitations and disadvantages of the related art.

Another object of the present invention is to provide a method for crystallizing an  
amorphous silicon layer and a method for fabricating a TFT using the same, so that the  
10 size of the silicon grain grows dramatically.

Another object of the present invention is to provide a method for crystallizing an  
amorphous silicon layer and a method for fabricating a TFT using the same, by forming a  
single crystalline silicon region on the glass substrate through a crystallization method  
that uses the SLS technique and improves the pattern of the laser beam.

15 Another object of the present invention is to provide a method for crystallizing an  
amorphous silicon layer and a method for fabricating a TFT using the same, by using a  
plurality of laser beams to crystallize a large area of the substrate in a relatively short  
period of time.

Additional features and advantages of the invention will be set forth in the  
20 description which follows and in part will be apparent from the description, or may be  
learned by practice of the invention. The objectives and other advantages of the  
invention will be realized and attained by the structure particularly pointed out in the  
written description and claims hereof as well as the appended drawings.

To achieve these and other advantages and in accordance with the purpose of the  
25 present invention, as embodied and broadly described, the present invention comprises  
the steps of preparing an amorphous silicon layer which will be crystallized on the  
insulating substrate; preparing a laser beam having a chevron-shape, the apex of which is  
round; first-irradiating a selected portion of the amorphous silicon layer with the laser  
beam at an energy density sufficient to induce complete melting; solidifying the melted  
30 region of the amorphous silicon layer by proceeding a lateral grain growth from the

unmelted regions to the melted regions; translating the amorphous layer relative to the beam image over a first distance to a first direction; and second-irradiating a selected portion of the amorphous silicon layer with the laser beam and crystallizing the irradiated portions of the amorphous silicon layer.

5 Another aspect of the present invention comprises steps of preparing an amorphous silicon layer which will be crystallized on the insulating substrate; preparing a plurality of laser beams, arrayed at a predetermined distance between the laser beams, each laser beam having a first directional straight line shape; first-irradiating a plurality of the selected portions of the amorphous silicon layer with the laser beams at an energy  
10 density sufficient to induce complete melting; solidifying the melted region of the amorphous silicon layer by proceeding a lateral grain growth from the unmelted regions to the melted regions; translating the amorphous layer relative to the beam image over a first distance to a second direction; and second-irradiating a plurality of the selected portions of the amorphous silicon layer with the laser beams at an energy density  
15 sufficient to induce complete melting and crystallizing the irradiated portions of the amorphous silicon layer.

Another aspect of the present invention comprises steps of preparing a mask having a plurality of opaque regions of the dot shape, arrayed at a predetermined distance between dot shape to dot shape in a transparent region substrate; preparing an  
20 amorphous silicon layer which will be crystallized on the insulating substrate; first-irradiating a selected region of the amorphous silicon layer with a laser beam at an energy density sufficient to induce complete melting, the laser beam being prepared by making an unpatterned laser beam pass through the mask; solidifying the melted region of the amorphous silicon layer by proceeding a lateral grain growth from the unmelted  
25 regions to the melted regions; translating the amorphous layer relative to the beam image over a distance less than the single-pulse lateral growth distance in a predetermined direction; second-irradiating a selected region of the silicon layer with a laser beam at an energy density sufficient to induce complete melting; solidifying the melted region of the silicon layer by proceeding a lateral grain growth from the unmelted regions to the

melted region; and translating the silicon layer relative to the beam image over a distance less than the single-pulse lateral growth distance in a predetermined direction.

Another aspect of the present invention comprises an active layer, a gate insulating layer and a gate electrode, the forming method of the active layer comprising the steps of: depositing an amorphous silicon layer on an insulating material layer;  
5 crystallizing the amorphous silicon layer with a laser beam by Sequential Lateral Solidification technique, the laser beam having a chevron-shaped aperture, the apex of which is round; and etching the amorphous silicon layer to pattern the active layer.

It is to be understood that both the foregoing general description and the  
10 following detailed description are exemplary and explanatory and are intended to provide further explanation of the invention as claimed.

Specific embodiments according to the present invention will now be described, by way of example, with reference to the accompanying drawings, in which:

Figs. 1A to 1D are schematic drawings showing a method for crystallizing a  
15 silicon layer according to a related art;

Fig. 2 is a schematic drawing showing a laser annealing system for the SLS technique;

Figs. 3A to 3C are schematic drawings illustrating a method for crystallizing a silicon layer according to a related art;

20 Figs. 4A to 4C are schematic drawings showing a method for crystallizing a silicon layer according to a related art;

Fig. 5 is a TEM showing the crystallization according to the related art;

Fig. 6 is a drawing showing the thermal distribution of the laser beam according to the related art;

25 Figs. 7A to 7C are schematic drawings for illustrating a method for crystallizing a silicon layer according to a first embodiment of the present invention;

Fig. 8 is a drawing showing thermal distribution of the laser beam according to the first embodiment of the present invention;

Fig. 9 is a TEM showing crystallization according to the first embodiment of the  
30 present invention;

Fig. 10 is a drawing showing crystallization according to a second embodiment of the present invention;

Fig. 11 is schematic drawing for illustrating a mask used in a third embodiment of the present invention;

5 Figs. 12A to 12C are schematic drawings showing a method for crystallizing a silicon layer according to the third embodiment of the present invention;

Figs. 13A to 13C are another schematic drawings showing a method for crystallizing a silicon layer according to the fourth embodiment of the present invention;

10 Fig. 14 is schematic drawing illustrating a mask used in a fifth embodiment of the present invention;

Figs. 15A to 15B are drawings showing crystallization according to the fifth embodiment of the present invention;

Fig. 16 is a schematic drawing showing another mask used in a sixth embodiment of the present invention;

15 Fig. 17 is a schematic drawing showing crystallization according to the sixth embodiment of the present invention;

Fig. 18A shows one of the various examples for the mask for patterning a laser beam according to a seventh embodiment of the present invention and Fig. 18B to Fig. 18C are schematic drawings for explaining a method for crystallizing a silicon film using  
20 th mask as shown in Fig. 18A.

Fig. 19 is a schematic drawing showing a mask used in a eighth embodiment of the present invention;

Figs. 20A to 20D are schematic drawings showing a method for crystallizing a silicon layer according to the eighth embodiment of the present invention;

25 Fig. 21 is a drawing showing crystallization according to the ninth embodiment of the present invention;

Fig. 22 is a schematic drawing illustrating a mask used in a tenth embodiment of the present invention;

30 Fig. 23 is a drawing showing crystallization according to the tenth embodiment of the present invention;



Fig. 24 is a schematic drawing showing a mask used in an eleventh embodiment of the present invention;

Fig. 25 is a drawing showing crystallization according to the eleventh embodiment of the present invention; and

5 Figs. 26A to 26E are schematic drawings showing a method for fabricating a TFT according to an embodiment of the present invention.

Figs. 7A to 7C are schematic drawings showing a method for crystallizing an amorphous silicon film using the SLS technique according to a first embodiment of the present invention, wherein an amorphous silicon film is crystallized by using a chevron-typed laser beam having a round apex.

Referring to Fig. 7A, a selected film region, having chevron-shaped apertures and a rounded apex 61, and bounded by the dashed lines, is irradiated at an energy density sufficient to induce complete melting. Subsequently, lateral grain growth proceeds from the unmelted regions to the adjacent narrow strip, fully-melted region. The grain boundaries in directionally solidified materials tend to form so as to always be approximately perpendicular to the melt interface. Due to such considerations, the maximum lateral growth distance that can be achieved with a single pulse is limited, depending on the film thickness and the incident energy density. Here, the grain formed at the apex of the chevron experiences lateral growth not only in the translation direction, but also transversely, because grain boundaries form perpendicularly to the melt interface. Thus, the negative curvature of the molten zone at the apex of the chevron leads to widening of the grain, so that a single crystal silicon grain region is induced.

25 The size of the single crystalline region is determined by the width of the beam pattern and the distance of the translation of the film. The location of the single crystalline region may be accurately controlled by using a laser beam which provides an appropriate pattern.

The difference of the thermal stress is reduced in the molten silicon portion irradiated by the rounded apex 61 of the laser beam. Fig. 8 shows the distribution of the

energy density of an irradiated silicon portion by a chevron-shaped laser beam having a rounded apex 61.

In particular, the entire portion of the energy density of the chevron-shaped film having a rounded apex has a normal energy distribution, of which the brim is low but is  
5 higher to the center. Accordingly, the difference of the thermal stress is not present in the molten silicon portion irradiated by the rounded apex of the laser beam. As a result, the interference of the laser beam in the apex is substantially reduced. Since the difference of the thermal stress is not formed or at least reduced in the molten silicon portion, the principal defect –the difference of the thermal stress, such as the sub-  
10 boundaries as above described – is not formed in the interior of the crystallized silicon.

Referring to Fig. 7B, the film is translated relative to the beam image over a distance of less than the single-pulse lateral growth distance, approximately one-half of this distance, and irradiated again. The lateral growth recommences from the edges of the completely molten region, one of which is located within the grains grown during the  
15 previous irradiation step. The length of the grains increases beyond the single-pulse lateral growth distance, and the grain formed at the apex of the chevron, namely, a single crystal silicon region, widens.

Referring to Fig. 7C, the above-cited process of irradiation and solidification, is repeated indefinitely, resulting in grains of any desired length. The final resultant  
20 microstructure is shown. The grain formed at the apex of the chevron, namely, the single crystal silicon region is widened dramatically.

Fig. 9 shows a TEM drawing, showing a crystalline silicon film crystallized according to the first embodiment of the present invention. This figure shows a polycrystalline silicon region, having lateral columnar silicon grains grown perpendicular  
25 to the interface between the amorphous silicon region and the irradiated silicon region, and a single crystalline region formed from the rounded apex of the chevron to its upper portion. A pure single crystalline region without sub-boundaries is shown.

Fig. 10 is a schematic drawing depicting a method for crystallizing a silicon film according to a second embodiment of the present invention. This embodiment uses a

laser beam having a continuous chevron-shape having rounded apexes 63. Fig. 10 shows the crystallization using the above laser beam.

The method for crystallizing the silicon film through the SLS technique by using the laser beam having the rounded apex is described previously. A plurality of the single crystalline regions, formed and extended from the plurality of the apexes, is shown. Thereby, a large area of the film can be crystallized in a short period of time according to the second embodiment of the present invention. Consequently, according to this second embodiment, a plurality of pure single crystalline silicon regions is formed on the glass substrate in a reduced time without significant defects.

Fig. 11 and Figs. 12A to 12C are schematic drawings showing a method for crystallizing a silicon film according to a third embodiment. Fig. 11 shows a mask forming a laser beam pattern and Figs. 12A to 12C show a crystallization process with the laser beam patterned by the mask shown in Fig. 11. In sum, three laser beams patterned by three transparent slits onto an amorphous silicon film is shown in Figs. 12A to 12C.

A plurality of transparent regions 112 having a long straight shape are arrayed at a predetermined distance in an opaque region substrate 111 of a mask 110 for patterning a laser beam. One laser beam is passed through the mask 110, having a plurality of transparent regions 112 to pattern a plurality of laser beams.

The size of the silicon grain formed by SLS technique is controlled by the distance of each transparent region 112 and the total number of the transparent regions 112 in the mask 110.

Referring to Fig. 12A, a plurality of the selected narrow regions having a straight slit shape of the film are first irradiated with a laser beam patterned by the mask shown in Fig. 11 at an energy density sufficient to induce complete melting. Subsequently, lateral grain growth proceeds from the unmelted regions of a-Si adjacent to the narrow strips which are fully-melted regions. The grain boundaries in directionally solidified materials are formed and are substantially perpendicular to the melted interface. Depending on the width of the molten region, lateral growth ceases when either (1) the two opposing growth fronts collide at the center, or (2) the molten region becomes sufficiently

supercooled so that a bulk nucleation of solids occur – whichever occurs first. Due to such considerations, the maximum lateral growth distance that can be achieved with a single pulse is limited to less than certain length, depending on the film thickness and the incident energy density.

5 As shown in Figs. 12A and 12B, a plurality of the polycrystalline regions 121 are formed by lateral grain growth and a plurality of the fine polycrystalline silicon regions 120-1 having bulk nucleation of solids are also formed.

Referring to Fig. 12B, the film is translated relative to the beam image over a distance less than the single-pulse lateral growth distance, and irradiated again. Lateral  
10 growth recommences from the edges of the completely molten region, one of which is located within the grains grown 121 during the previous, the first irradiation, step. The length of the grains is increased beyond the single-pulse lateral growth distance. Reference numeral 122 denotes a plurality of polycrystalline regions formed by lateral grain growth and 120-2 denotes a plurality of the fine polycrystalline silicon regions  
15 having bulk nucleation of solids.

Referring to Fig. 12C, the above-cited process, irradiating and solidifying, can be repeated indefinitely, leading to grains of any desired length. The final microstructure obtained in this fashion is shown. A plurality of the grains having a predetermined length, such as the distance between one transparent region and an adjacent transparent  
20 region of the mask, are shown in Fig 12C. Polycrystalline silicon regions 125-1, 125-2 and 125-3 crystallized by SLS, using a plurality of the laser beams are also shown in Fig. 12C.

Accordingly, the large area film is crystallized in short time according to the third embodiment of the present invention. The larger the number of laser beams, the shorter  
25 the crystallization time. In other words, the crystallization speed by using n laser beams is n-times faster than that by using only one laser beam. A plurality of the crystalline silicon regions is formed on the glass substrate in short time according to the third embodiment of the present invention

Figs. 13A - 13C are schematic drawings for explaining a method for crystallizing  
30 a silicon film according to a fourth embodiment of the present invention, in which the

laser scanning for the lateral grain growth by SLS are performed two times, for example, once in a vertical direction and then a horizontal direction. The first laser scanning for lateral grain growth proceeds at a first direction to form a polycrystalline silicon region having first direction columnar grains and the second laser scanning for lateral grain growth proceeds at a second direction perpendicular to the first direction to form single crystalline regions by using one of the first direction columnar grains as seed for grain growth.

Referring to Fig. 13A, a plurality of the selected straight regions of the crystallized silicon film having first direction (I) columnar grains are irradiated with a plurality of the straight laser beams having the second direction perpendicular to the first direction. The straight laser beams are prepared by rotating the mask which was used in the third embodiment of the present invention by an angle of about 90 degrees. Subsequently, the lateral grain growth proceeds in the second direction (II) from the unmelted regions to the adjacent narrow strips, fully-melted regions by using one of the first direction columnar grains as a seed for grain growth. The grain boundaries in directionally solidified materials tend to form so as to always be approximately perpendicular to the melted interface. As a result, polycrystalline silicon regions 125-1, 125-2 and 125-3 crystallized by a method according to the third embodiment of the present invention are formed.

Referring to Fig. 13B, the above-cited process, irradiating and solidifying, can be repeated indefinitely, leading to grains of any desired length. As the lateral grain growth in the second direction proceeds, the seed grain grows more and more. The translating direction of the laser beam relative to the film is in the second direction (II).

Referring to Fig. 13C, a plurality of the seed grain grows dramatically to form a plurality of the single crystalline region G as a result of the crystallization. Accordingly, crystallized silicon film having very large size single crystalline regions could be formed on the glass substrate.

Fig. 14 and Fig. 15A to Fig. 15B are schematic drawings for explaining a method for crystallizing a silicon film according to a fifth embodiment of the present invention.

Fig. 14 shows a mask forming a laser beam pattern and Fig. 15A to Fig. 15B show a crystallized silicon film with laser beams patterned by the mask shown in Fig. 14.

A plurality of transparent regions or slits 142 having a long straight shape are arrayed in a row and in a column at a predetermined distance in opaque region substrate

5 141 of a mask 140 for patterning a laser beam.

The crystallized silicon film having a plurality of the single crystalline silicon regions arrayed in a row and in a column as shown in Fig. 15A to Fig. 15B is formed when the above-cited process, irradiating and solidifying, is repeated indefinitely, leading to grains of any desired length as in the fourth embodiment of the present invention. The

10 crystallized silicon film having a plurality of the columnar long grain as shown in Fig. 15A is formed by proceeding lateral grain growth in the first direction. The crystallized silicon film having a plurality of the single crystalline silicon region G as shown in Fig. 15B is formed by proceeding lateral grain growth in the first direction and then in the second direction perpendicular to the first direction.

15 Fig. 16 and 17 are schematic drawings for explaining a method for crystallizing a silicon film according to a sixth embodiment of the present invention. Fig. 16 shows a mask for forming a laser beam pattern and Fig. 17 shows a crystallized silicon film with laser beams patterned by the mask shown in Fig. 16. The sixth embodiment of the present invention discloses the method for crystallizing the large area of a silicon film by

20 SLS with a plurality of the laser beams having a continuous chevron-shape, the apexes of which are rounded as described in the second embodiment of the present invention.

A plurality of transparent regions 162 having a continuous chevron-shape of which the apexes are rounded as described in the second embodiment of the present invention are arrayed in a row or in a column at a predetermined distance in an opaque

25 region substrate 161 of a mask 160 for patterning a laser beam. Accordingly, one laser beam is passed through the mask 160 having a plurality of transparent regions 162 to pattern a plurality of laser beams.

The crystallized silicon film having a plurality of the single crystalline silicon regions arrayed in a row or in a column as shown in Fig. 17 is formed when the above-

cited process, irradiating and solidifying, is repeated, leading to grains of any desired length as in the second embodiment of the present invention.

The method for crystallizing the silicon film through SLS technique by using the laser beam having a continuous chevron-shape, the apexes of which are rounded, is described previously. A plurality of the single crystalline regions, formed and extended from the plurality of the apexes, are shown. As a result, the large area film is crystallized in short time by using a plurality of the laser beams according to the third to sixth embodiments of the present invention to form a large size of the silicon grain or single crystalline silicon region.

As described, the number and the pattern of the laser beams are controlled by the condition of the pattern of the transparent regions of the mask. Accordingly, the number and the pattern of the laser beams may be chosen accordingly to different applications.

Fig. 18A shows one of the various examples for the mask for patterning a laser beam. according to the seventh embodiment of the present invention and Fig. 18B to Fig. 18C are schematic drawings for explaining a method for crystallizing a silicon film using the mask as shown in Fig. 18A.

The mask 180 has a first region 181 and a second region 182 in which a plurality of transparent regions are arrayed in opaque region substrate 180-1 for patterning a laser beam.

A plurality of long straight line shaped transparent regions 180-2 are arrayed in a first direction 189-1 at a first distance  $d_1$  in the first region 181. A plurality of long horizontal line shaped transparent regions 180-3 are arrayed in a second direction 189-2 at a second distance  $d_2$  in the second region 182. The first direction 189-1 is nearly perpendicular to the second direction 189-2.

One laser beam is passed through the mask 180 to pattern a plurality of laser beams.

Referring to Fig. 18B, the first region 181 of the mask 180 is aligned at a first base line 187-1 of a silicon layer.

And then, one laser beam is passed through only the first region 181 of the mask 180 to be patterned to a plurality of long straight line shaped laser beams. And, the silicon layer is first crystallized by SLS using the mask by carrying out the cited process in the third embodiment as shown in Fig. 12A to 12C in which irradiating and solidifying are repeated indefinitely.

The first SLS crystallization region 183-1 is formed by translating the silicon layer relative to the laser beams at a first distance  $d_1$  during proceeding the SLS process by more than two shots of the laser. Herein, the first arrow 188-1 shows a direction of moving the mask 180 and the laser beams relative to the silicon layer.

And then, the first region 181 of the mask 180 is aligned at a sequin base line 187-2 of the silicon layer. And, a second SLS crystallization region 183-2 is formed by carrying out the same process as is applied for forming the first SLS crystallization region

These crystallization process is carried out repeatedly through the all substrate to form a plurality of the SLS crystallization regions.

The last SLS crystallization region 183-3 is shown in Fig 18B.

Referring to Fig. 18C, the second region 182 of the mask 180 is aligned at a third base line 187-3 of the silicon layer.

And, one laser beam is passed through only the second region 182 of the mask 180 to be patterned to a plurality of long horizontal line shaped laser beams. And then, the SLS silicon layer is second crystallized by SLS using the mask 180 by carrying out the cited process in the fourth embodiment as shown in Fig. 13A to 13C in which irradiating and solidifying are repeated indefinitely.

The first SLS single crystallization region 185-1 is formed by translating the SLS silicon layer relative to the laser beams at a second distance  $d_2$  during proceeding the SLS process by more than two shots of the laser. Herein, the second arrow 188-2 shows a direction of moving the mask 180 and the laser beams relative to the silicon layer.

And then, the second region 182 of the mask 180 is aligned at a fourth base line 187-4 of the silicon layer. And, a second SLS single crystallization region 185-2 is



formed by carrying out the same process as is applied for forming the first SLS single crystallization region

These crystallization process is carried out repeatedly through the all substrate to form a plurality of the SLS single crystallization regions.

5 The last SLS single crystallization region 185-3 is shown in Fig 18C.

Fig. 19 and Figs. 20A to 20D are schematic drawings for explaining a method for crystallizing a silicon film according to an eighth embodiment of the present invention. Fig. 19 shows a mask forming a laser beam pattern and Figs. 20A to 20D show a crystallization process with laser beams patterned by the mask shown in Fig. 19.

10 A plurality of opaque regions 190-2 having a plurality of the dot shape are arrayed at a predetermined distance in a transparent region substrate 190-1 of a mask 190 for patterning a laser beam. Herein, the shape of the dot may be a circle shape or a hexagon shape and the like. For convenience, the phenomenon that the film is crystallized by one laser beam is described. Referring to Fig. 20A, a portion except the  
15 selected dot region is first irradiated with a laser beam patterned by the mask shown in Fig. 19 at an energy density sufficient to induce complete melting.

The amorphous film is translated relative to the laser beam to the first direction over a distance less than a predetermined distance for second irradiating. While the film is translating, lateral grain growth proceeds from the unmelted regions, the dot region  
20 201 which remains amorphous silicon adjacent to the fully-melted regions. The dot region 201, the unmelted amorphous silicon, is used as seeds and grows into the fully-melted regions. The grain boundaries in directionally solidified materials tend to form so as to always be approximately perpendicular to the melt interface. Reference number 202 denotes a dot region which will not be irradiated with second irradiating.

25 Depending on the width of the molten region, lateral growth ceases when either (1) the two opposing growth fronts collide at the center, or (2) the molten region becomes sufficiently supercooled so that bulk nucleation of solids occur – whichever occurs first. Due to such considerations, the maximum lateral growth distance that can be achieved with a single pulse is limited to less than a certain length, depending on the film thickness  
30 and the incident energy density.

Referring to Fig. 20B, a portion except the selected dot region is irradiated for the second time at an energy density sufficient to induce complete melting. Herein the dot region is located in the crystallized silicon region. In this embodiment, the film is translated relative to the laser beam over a distance less than one pulse lateral grain growth.

The dot region which is not irradiated remains solid state of the polycrystalline silicon and has a limited number of silicon grains. In Fig. 20B, reference number 203 denotes a dot region which will not be irradiated with a third irradiating.

The amorphous film is translated relative to the laser beam to the second direction over a distance less than predetermined distance for third irradiating. While the film is translating, lateral grain growth proceeds from the unmelted regions, dot region 202 which remains polycrystalline silicon adjacent to the fully-melted regions. The dot region 202, which is unmelted polycrystalline silicon having three grains, is used as a seed and grows into the fully-melted regions. The grain boundaries in directionally solidified materials tend to form substantially perpendicular to the melted interface. Reference number 203 denotes a dot region which will not be irradiated with a third irradiation.

Referring to Fig. 20C, a portion except the selected dot region is irradiated at a third time at an energy density sufficient to induce complete melting. Herein the dot region is located in the crystallized silicon region. The film is translated relative to the laser beam over a distance less than one pulse lateral grain growth.

The dot region which is not irradiated remains solid state of the polycrystalline silicon and has a limited number of silicon grains than the previous step.

Accordingly, the more the above process carries out, the less the number of the grain is in dot region. Finally, the single grain remains in the dot region when the above processes are carried out an appropriate number of times.

The amorphous film is translated relative to the laser beam to a fourth direction over a distance less than predetermined distance for irradiating four. While the film is translating, lateral grain growth proceeds from the unmelted regions, dot region 203 in which the single crystalline grain remains adjacent to the fully-melted regions. The

unmelted single crystalline silicon dot region 203 is used as a seed and grows into the fully-melted regions. The single grain grows laterally. Reference number 204 denotes a dot region which will not be irradiated during the fourth irradiation.

Referring to Fig. 20D, a portion except the selected dot region 204 is fourth  
5 irradiated at an energy density sufficient to induce complete melting. Herein the dot region is located in the crystallized silicon region. The film is translated relative to the laser beam over a distance less than one pulse lateral grain growth. The dot region 204 which is not irradiated remains solid state of the single crystalline silicon.

The film is translated relative to the laser beam to the next direction over a  
10 distance less than predetermined distance for next irradiating. While the film is translating, lateral grain growth proceeds from the unmelted regions which constitutes the dot region 204 in which the single crystalline grain remains adjacent to the fully-melted regions. The unmelted single crystalline silicon dot region 204 is used as a seed and grows into the fully-melted regions. The single grain grows laterally.

15 Accordingly, as the above process is repeatedly carried out, the number grains is reduced in dot region. Finally, the single grain remains in the dot region when the above process is sufficiently carried out.

The size of the single crystalline silicon region is the same as the lateral grain growth. The length of the lateral grain growth depends on the thickness of the film and  
20 the temperature of the film. Accordingly, the size of the single crystalline silicon region could be controlled according to the thickness and the temperature of the film.

In the case when the distance between the dot to dot is larger than the length of the lateral grain growth, each of the large single crystalline silicon regions is separated. This is why the length of the lateral grain growth is arranged to be larger than the  
25 distance between the dot to the dot. Accordingly, when the distance between the dots is smaller than the length of the lateral grain growth, the polycrystalline silicon film having a plurality of the single crystalline silicon regions, each region having a grain boundary which is at the interface of the silicon grain and the next silicon grain.

In the above description, the case of the four irradiating and crystallization steps  
30 are described as embodiments of the present invention. However, depending on the

process condition, the larger single crystalline silicon grain could be formed by using combination of one or more irradiating crystallization methods.

Fig. 21 shows the crystallization result of the silicon film according to the ninth embodiment of the present invention.

5           In the case when the distance between the dot to the dot is larger than the length of the lateral grain growth, each of the large single crystalline silicon regions is separated and fine crystalline silicon is shown.

          However, when the distance between the dots is smaller than the length of the lateral grain growth, the polycrystalline silicon film having a plurality of the single  
10   crystalline silicon regions, each region having a grain boundary which is at the interface of the silicon grain and the next silicon grain.

          The shape and the location of the single crystalline silicon region is controlled by the array of the opaque regions of the mask. This is described referring to the following eighth and ninth embodiments of the present invention.

15           Figs. 22 and 23 are schematic drawings showing a method for crystallizing a silicon film according to a tenth embodiment of the present invention. Fig. 22 shows a mask forming a laser beam pattern and Fig. 23 shows a crystallization result with laser beam patterned by the mask shown in Fig. 22.

          In this embodiment, a plurality of opaque regions 220-2 having a plurality of the  
20   dot shape are arrayed at a predetermined distance in a transparent region substrate 220-1 of a mask 190 for patterning a laser beam. The one dot is surrounded by the six dots. Herein, the shape of the dot is a circle shape or a hexagon shape and the like. The crystallized silicon film having a plurality of the hexagonal shaped single crystalline silicon regions arrayed is formed when the above cited process, irradiating and  
25   solidifying, is repeated indefinitely, leading to grains of any desired length as in the seventh embodiment of the present invention. The one lateral growing silicon grain collides to the other six lateral growing silicon grains.

          Figs. 24 and 25 are schematic drawings for explaining a method for crystallizing a silicon film according to an eleventh embodiment of the present invention. Fig. 24

shows a mask forming a laser beam pattern and Fig. 25 shows a crystallization result with laser beam patterned by the mask shown in Fig. 24.

In this embodiment, a plurality of opaque regions 240-2 having a plurality of the dot shape are arrayed at a predetermined distance in a transparent region substrate 240-1 of a mask 240 for patterning a laser beam. The one dot is surrounded by the four dots. Herein, the shape of the dot is a circle shape or a hexagon shape and the like. The crystallized silicon film having a plurality of the square shaped single crystalline silicon regions arrayed is formed when the above cited process, irradiating and solidifying, is repeated indefinitely, leading to grains of any desired length as in the seventh embodiment of the present invention. The one lateral growing silicon grain collides to the other four lateral growing silicon grains.

The higher the temperature of the film, the longer the solidification time. Accordingly, the length of the lateral grain growth is larger to form the larger single crystalline silicon region.

Accordingly, the shape and the size of the single crystalline silicon region is controlled by the array of the opaque regions of the mask, temperature of the film and the translating direction and the translating speed of the film relative to the laser beam.

When the distance between the opaque region to the opaque region of the mask is uniform, the size of the single crystalline silicon region is uniform, as in the eighth and ninth embodiment of the present invention.

Figs. 26A to 26E are schematic drawings for explaining a method for fabricating a TFT according to an embodiment of the present invention. A co-planar-typed TFT having a pixel electrode is taken as an example.

Referring to Fig. 26A, a buffer layer 261, such as a silicon oxide layer, is deposited on the insulating substrate 260, such as a glass substrate. An amorphous silicon layer is deposited on the buffer layer 261 and is crystallized by the SLS technique. Since the SLS technique is shown in the description of the first to ninth embodiments of the present invention, the description of the SLS technique will not be repeated. The buffer layer prevents the impurities of the insulating substrate to penetrate into the silicon thin film during the process of crystallization. A crystallized silicon thin film 262

having a dramatically large first silicon grain 262-1, a second silicon grain 262-2 and a third silicon grain 262-3 are shown in Fig 26A.

Referring to Fig. 26B, the crystallized silicon thin film is etched by photolithography to form an active layer 263. Since the first silicon grain, the second silicon grain and the third silicon grain are large, the active layer is formed of one single silicon grain. Accordingly, a single crystalline silicon TFT could be fabricated on the insulating substrate, such as a glass substrate.

Referring to Fig. 26C, a first insulating layer and a first conductive layer are subsequently formed on the exposed surface of the substrate comprising the active layer 263. The first conductive layer is etched by photolithography to form a gate electrode 265 and the first insulating layer is etched to form a gate insulating interlayer 264. Source and drain regions 263S and 263D are formed by doping impurities in the exposed portions of the active layer. The channel region 263c is located between the source and the drain regions.

Referring to Fig. 26D, a second insulating layer 266 is deposited on the exposed surface of the substrate comprising the gate electrode. The second insulating layer is etched selectively by photolithography to expose the portions of the source and the drain regions. A second conductive layer is deposited on the exposed surface of the substrate and is etched selectively by photolithography to form source and drain electrodes 267S and 267D.

Referring to 26E, a third insulating layer 268 is deposited on the exposed surface of the substrate comprising the source and drain electrodes. The third insulating layer 268 is etched selectively by photolithography to expose the portion of the drain electrode 267D. And a transparent conductive layer is deposited on the exposed surface of the substrate and is etched selectively by photolithography to form a pixel electrode 269.

As described, a large single crystalline silicon region without defects could be formed in the insulating substrate by SLS technique with a laser beam having a chevron shape, the apex of which is round. And a large area silicon film is crystallized by SLS technique in short time by using a plurality of the laser beam.

The crystallized silicon film having a plurality of the large single crystalline silicon regions could be formed by proceeding lateral grain growth at the first direction to form a silicon film having a plurality of the columnar long grain and proceeding lateral grain growth at the second direction perpendicular to the first direction through using the columnar long grain as seed for grain growth.

The size, pattern and location of the single silicon grain are decided by controlling the pattern of the laser beam and the translation distance of the silicon layer relative to the laser beam.

The embodiments enable one to fabricate a System-On-Panel (SOP) type LCD, in which a pixel part, a driver, a controller and a CPU circuit share the same substrate, whereby the fabrication process is simplified and the productivity is increased. Moreover, it is possible to fabricate portable LCD products, reduced in weight and size, since the space occupied by the controller and the CPU circuit is reduced.

It will be apparent to those skilled in the art that various modifications and variations can be made in a SOP-type liquid crystal display of the embodiments without departing from the spirit or scope of the inventions. Thus, it is intended that the present invention will cover the modifications and variations of this invention provided they come within the scope of the appended claims and equivalents.

It will be apparent to those skilled in the art that various modifications and variations can be made in the method for crystallizing an amorphous silicon layer and the method for fabricating a thin film transistor using the same of the embodiments without departing from the spirit or scope of the inventions. Thus, it is intended that the present invention covers the modifications and variations of the illustrated embodiments provided they come within the scope of the appended claims and equivalents.

CLAIMS

1. A method for crystallizing an amorphous silicon layer disposed on a substrate, the method comprising:
  - preparing a first laser pattern comprising a plurality of chevron-shaped lines, wherein the chevron-shaped lines are aligned in a first direction;
  - first-irradiating the amorphous silicon layer with a laser beam at an energy density sufficient to induce substantially complete melting to form a melted region and an unmelted region;
  - solidifying the melted region of the amorphous silicon layer with a lateral grain growth from the unmelted region to the melted region;
  - translating the amorphous silicon layer relative to the first laser pattern over a first distance; and
  - second-irradiating the amorphous silicon layer with the laser beams at an energy density sufficient to induce complete melting and crystallizing the irradiated portions of the amorphous silicon layer.
2. The method of claim 1, wherein the irradiating-solidifying-translating the amorphous silicon layer is performed at least two times.
3. The method of claim 1 or 2, further comprising the steps of preparing a second laser pattern comprising a plurality of chevron-shaped lines in a second direction perpendicular to the first direction of the first laser pattern and repeating the irradiating-solidifying-translating of the amorphous silicon layer using the second laser pattern at least two times.
4. The method of any preceding claim, wherein the first laser pattern is prepared by emitting a laser beam through a mask, the mask having a plurality of transparent regions in an opaque substrate.
5. The method of any preceding claim, wherein each one of the chevron-shaped lines has a curved apex.



6. A method for crystallizing an amorphous silicon layer, the method comprising:  
preparing an amorphous silicon layer to be crystallized on a substrate;  
preparing a plurality of laser beams arrayed with a predetermined distance between the laser beams, each laser beam having continuous chevron-shapes with rounded apexes;  
first-irradiating a plurality of the selected portions of the amorphous silicon layer with the laser beams at an energy density sufficient to induce substantially complete melting to form a melted region and an unmelted region;  
solidifying the melted region of the amorphous silicon layer with a lateral grain growth from the unmelted region to the melted region;  
translating the amorphous layer relative to the beam image over a first distance in a second direction; and  
second-irradiating a plurality of the selected portions of the amorphous silicon layer with the laser beams at an energy density sufficient to induce complete melting and crystallizing the irradiated portions of the amorphous silicon layer.

7. A method for fabricating a TFT which comprises an active layer, a gate insulating layer and a gate electrode, the method comprising:  
depositing an amorphous silicon layer on an insulating material layer;  
crystallizing the amorphous silicon layer with a laser beam by sequential lateral solidification technique, the laser beam shape having a plurality of chevrons with rounded apexes; and  
etching the amorphous silicon layer to pattern the active layer.

8. The method of claim 7, further comprising the steps of forming a gate electrode and a gate insulating layer on the active layer and forming source and drain regions in the active layer.

9. A method for crystallizing an amorphous silicon layer disposed on a substrate, the method comprising:

irradiating the amorphous silicon layer with an energy source at an energy density sufficient to induce substantially complete melting to form a melted region and an unmelted region, wherein the energy source has a first pattern comprising a plurality of lines arranged adjacent to each other and separated by a predetermined distance in a first direction;

solidifying the melted region of the amorphous silicon layer with a lateral grain growth from the unmelted region to the melted region;

translating the amorphous silicon layer relative to the energy source over a first distance, wherein the translation of the energy source overlaps the melted region of the amorphous silicon layer; and

irradiating the amorphous silicon layer with the laser beams at an energy density sufficient to induce complete melting and crystallizing the irradiated portions of the amorphous silicon layer.

10. The method of claim 9, wherein the energy source has a second pattern comprising a plurality of lines arranged adjacent to each other and separated by a predetermined distance in a second direction.

11. The method of claim 9 or 10, wherein the energy source is prepared by emitting a laser beam through a mask, the mask having a plurality of transparent regions.

12. The method of any of claims 9 to 11, wherein each one of the plurality of lines of the first pattern includes chevron-shaped lines with curved apexes.

13. The method of any of claims 9 to 11, wherein each one of the plurality of lines of the first pattern is rectangular.

14. The method of claim 10, wherein each one of the plurality of lines of the second pattern includes chevron-shaped lines with curved apexes.

15. The method of claim 10, wherein each one of the plurality of lines of the second pattern is rectangular.

16. The method of any of claims 9 to 15, further comprising rotating the energy source with respect to the amorphous silicon layer so that first pattern is arranged in a second direction which is substantially perpendicular to the first direction.

17. The method of claim 16, wherein the energy source is prepared by emitting a laser beam through a mask and the rotating the energy source comprises rotating the mask.

18. A TFT manufactured according to the process of any one of the preceding claims.

19. A method for crystallizing an amorphous silicon layer as substantially described herein with reference to and/or substantially as illustrated in Figs. 7A to 25 of the accompanying drawings.

20. A method for fabricating a thin film transistor as substantially described herein with reference to and/or substantially as illustrated in Figs. 26A to 26E of the accompanying drawings.

21. A method substantially in accordance with any of the first to eleventh embodiments described and/or illustrated herein.



Application No: GB 9913336.5  
Claims searched: 1-18

Examiner: SJ Morgan  
Date of search: 29 September 1999

**Patents Act 1977**  
**Search Report under Section 17**

**Databases searched:**

UK Patent Office collections, including GB, EP, WO & US patent specifications, in:

UK CI (Ed.Q): H1K (KLHA, KLXW)

Int CI (Ed.6): H01L 21/20

Other: Online: WPI, JAPIO, EPODOC, INSPEC

**Documents considered to be relevant:**

Category	Identity of document and relevant passage	Relevant to claims
A	WO 97/45827 A1 (COLUMBIA UNIVERSITY)	
A	US 5 496 768 (CASIO)	
X	"Single-crystal Si films for thin-film transistor devices", Applied Physics Letters, Vol 70, No. 25, 23/6/97, pp 3434-3436, SI James, RS Sposili, and MA Cowder.	1-4, 9-11, & 18
X	"Sequential lateral solidification of thin silicon films on SiO <sub>2</sub> ", Applied Physics Letters, Vol 69, No 19, 4/11/96, pp 2864-2866, RS Sposili & SI James.	9-11, 13, 15, & 18

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